



Embedded SoPC Design with Nios II Processor and Verilog Examples

Pong P. Chu

Download now

[Click here](#) if your download doesn't start automatically

Embedded SoPC Design with Nios II Processor and Verilog Examples

Pong P. Chu

Embedded SoPC Design with Nios II Processor and Verilog Examples Pong P. Chu

Explores the unique hardware programmability of FPGA-based embedded systems, using a learn-by-doing approach to introduce the concepts and techniques for embedded SoPC design with Verilog

An SoPC (system on a programmable chip) integrates a processor, memory modules, I/O peripherals, and custom hardware accelerators into a single FPGA (field-programmable gate array) device. In addition to the customized software, customized hardware can be developed and incorporated into the embedded system as well—allowing us to configure the soft-core processor, create tailored I/O interfaces, and develop specialized hardware accelerators for computation-intensive tasks.

Utilizing an Altera FPGA prototyping board and its Nios II soft-core processor, Embedded SoPC Design with Nios II Processor and Verilog Examples takes a "learn by doing" approach to illustrate the hardware and software design and development process by including realistic projects that can be implemented and tested on the board.

Emphasizing hardware design and integration throughout, the book is divided into four major parts:

- Part I covers HDL and synthesis of custom hardware
- Part II introduces the Nios II processor and provides an overview of embedded software development
- Part III demonstrates the design and development of hardware and software of several complex I/O peripherals, including a PS2 keyboard and mouse, a graphic video controller, an audio codec, and an SD (secure digital) card
- Part IV provides several case studies of the integration of hardware accelerators, including a custom GCD (greatest common divisor) circuit, a Mandelbrot set fractal circuit, and an audio synthesizer based on DDFS (direct digital frequency synthesis) methodology

While designing and developing an embedded SoPC can be rewarding, the learning can be a long and winding journey. This book shows the trail ahead and guides readers through the initial steps to exploit the full potential of this emerging methodology.

 [Download Embedded SoPC Design with Nios II Processor and Ve ...pdf](#)

 [Read Online Embedded SoPC Design with Nios II Processor and ...pdf](#)

Download and Read Free Online Embedded SoPC Design with Nios II Processor and Verilog Examples Pong P. Chu

From reader reviews:

Olivia Clinard:

Book is to be different for every grade. Book for children right up until adult are different content. As we know that book is very important for people. The book Embedded SoPC Design with Nios II Processor and Verilog Examples was making you to know about other expertise and of course you can take more information. It is very advantages for you. The book Embedded SoPC Design with Nios II Processor and Verilog Examples is not only giving you considerably more new information but also to become your friend when you truly feel bored. You can spend your own spend time to read your e-book. Try to make relationship with all the book Embedded SoPC Design with Nios II Processor and Verilog Examples. You never really feel lose out for everything in the event you read some books.

Cheryl Kirkland:

Information is provisions for anyone to get better life, information nowadays can get by anyone from everywhere. The information can be a expertise or any news even a huge concern. What people must be consider if those information which is inside the former life are hard to be find than now is taking seriously which one is suitable to believe or which one the particular resource are convinced. If you obtain the unstable resource then you have it as your main information you will have huge disadvantage for you. All of those possibilities will not happen throughout you if you take Embedded SoPC Design with Nios II Processor and Verilog Examples as the daily resource information.

Jeremy Turner:

Your reading sixth sense will not betray you, why because this Embedded SoPC Design with Nios II Processor and Verilog Examples e-book written by well-known writer who really knows well how to make book that can be understand by anyone who have read the book. Written inside good manner for you, leaking every ideas and writing skill only for eliminate your own hunger then you still skepticism Embedded SoPC Design with Nios II Processor and Verilog Examples as good book but not only by the cover but also with the content. This is one e-book that can break don't determine book by its deal with, so do you still needing yet another sixth sense to pick this kind of!? Oh come on your reading through sixth sense already alerted you so why you have to listening to yet another sixth sense.

Sheila Dickerson:

On this era which is the greater person or who has ability in doing something more are more treasured than other. Do you want to become one among it? It is just simple solution to have that. What you are related is just spending your time not very much but quite enough to possess a look at some books. One of the books in the top checklist in your reading list is definitely Embedded SoPC Design with Nios II Processor and Verilog Examples. This book and that is qualified as The Hungry Slopes can get you closer in growing to be precious person. By looking upwards and review this reserve you can get many advantages.

Download and Read Online Embedded SoPC Design with Nios II Processor and Verilog Examples Pong P. Chu #HRIE860XBFN

Read Embedded SoPC Design with Nios II Processor and Verilog Examples by Pong P. Chu for online ebook

Embedded SoPC Design with Nios II Processor and Verilog Examples by Pong P. Chu Free PDF d0wnl0ad, audio books, books to read, good books to read, cheap books, good books, online books, books online, book reviews epub, read books online, books to read online, online library, greatbooks to read, PDF best books to read, top books to read Embedded SoPC Design with Nios II Processor and Verilog Examples by Pong P. Chu books to read online.

Online Embedded SoPC Design with Nios II Processor and Verilog Examples by Pong P. Chu ebook PDF download

Embedded SoPC Design with Nios II Processor and Verilog Examples by Pong P. Chu Doc

Embedded SoPC Design with Nios II Processor and Verilog Examples by Pong P. Chu Mobipocket

Embedded SoPC Design with Nios II Processor and Verilog Examples by Pong P. Chu EPub